7/6/2011 Karu-upg KI-11 121 B.E ETRX VIII (Rev) Embedded System 4-lead Time Peogleanmin

Con. 3893-11.

(REVISED COURSE)

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1.12	Hours)	ì
10	Hours	ŗ
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[Total Marks: 100

N.B.: (1) Question No. 1 is c	ompulsory.
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- (2) Attempt any four from the remaining six questions.
- (a) Suggest various techniques used for inter process communication in an 15 embedded system with relevant examples. Also, explain strategies used for synchronization between processes.
 - (b) With the help of a neat diagram, explain the different states a task can be in 5 and the transitions between them.
- 2. (a) Explain the various operating modes of the ARMY processor. 10
 - (b) What is the Shard Data Problem ? Explain various techniques to overcome 10 it. (With relevant examples).
- (a) Explain the interface of Alphanumeric LCD with any microcontroller of your 7 choice. (Draw neat diagram)
 - (b) Write a detailed note on the CAN Bus explaining its features and protocol. 7
 - (c) Differentiate between CISC and RISC processors. 6
- (a) What is interrupt latency in Embedded systems ? Suggest methods to reduce 10 latency.
 - (b) Explain what is the Linear sequential model in Embedded software 10 development.
- (a) Explain the Register set of the MSPHZO RISC controller (working Registers, 10 SFRs, status Register etc.)
 - (b) Write a detailed note on the THUMB mode of operation of the ARMY processor. 10
- (a) Explain Bounded and unbanded priority Inversion problem. Suggest methods 10 to overcome / minimise it.
 - (b) Explain the various program modelling techniques used in Embedded system 10 design.

7. Write short notes on :-

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- (a) Watching Timer
- (b) Serial Peripheral Interface (SPI)
- (c) Different types of memories in Embedded systems
- (d) Digital Signal Controllers (DSCs).