Power Electronics. Per 5/6/12

May 2012

1-1st Half-12 mina (Con.4720)

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Con. 4720-12.						215
				(3 Hours)	[Total Marks :	100
	N.	В.:	(1) (2) (3) (4)	Question No. 1 is <b>compulsory</b> .  Attempt any <b>four</b> questions from remaining <b>six</b> question <b>Figures</b> to the <b>right</b> indicate <b>full</b> marks.  Assume <b>suitable</b> data, if <b>any</b> .	IS.	
1.	Atte	empt (a)		following:— blain briefly, the 'SOA' rating of the Power Transistors.		20
		(b)	Exp	plain the importance of $\frac{dv}{dt}$ and $\frac{di}{dt}$ rating along with p	roper protection	
		(c) (d)	Stat con Exp	euit in case of SCR.  Ite the conditions for getting inversion mode operation  Inmutated converters.  Itelain the need of commutation in thyristor circuits.  Inmutation and current commutation?		
2.	• •	Explain with neat circuit diagram and associated waveforms the operation of single phase semiconverter with RL load. Derive the expression for average load voltage, average load current and RMS load voltage. Explain with neat diagram the full wave A.C. phase control using Triac and Diac. Draw waveforms.				-
<ul> <li>3. (a) A 3 phase half wave controlled rectifier is operated on a 3 phase A.C. supply an RMS phase voltage of 230 volts and f = 50 Hz. The load resistance R = For an average output voltage of 40% of the maximum possible output vo Calculate:— <ul> <li>(i) Delay angle α</li> <li>(ii) RMS and average load currents</li> <li>(iii) RMS and average thyristor currents.</li> </ul> </li> <li>(b) Explain UJT triggering circuit for triggering of 2 SCR's used in full wave continuous.</li> </ul>				stance $R = 10 \Omega$ . e output voltage,	10	
	(1)	rectifier with proper isolation. What is the need of synchronization circuit?		10		
4.	(a) (b)	For follo	a sir	with neat circuit diagram the operation of static D.C. cingle phase fully controlled bridge rectifier, derive the experiormance factors:—  D.C. Voltage ratio  Current distortion factor  Harmonic factor  Input displacement factor		10 10

(v) Voltage ripple factor.

## Con. 4720-GN-9215-12.

- (a) Explain the operation of 3 phase fully controlled rectifier with resistive load. Draw 10 the various waveforms for α = 30°.
  - (b) Explain latchup in IGBT. How does latchup take place and how to avoid it? 10
- 6. (a) Explain with neat diagram and associated waveforms the operation of auxiliary 10 voltage commutation.
  - (b) What is half waving effect in case of single phase half wave controlled rectifier 5 with RL load?
  - (c) A half wave controlled rectifier is connected to a 120 V source. Calculate the 5 firing angle necessary to deliver 150 W of power to a 10  $\Omega$  load.

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- 7. Write short notes on :-
  - (a) Soft start method
    - (b) Dynamic (Turn ON and Turn OFF) Characteristics of SCR
    - (c) Types of cooling of a power semiconductor device.