Time: 3 Hours

B. Tech. Degree VII Semester Examination November 2013

EE 705 (B) HIGH VOLTAGE DC TRANSMISSION

PART A

(2006 Scheme)

Maximum Marks: 100

(Answer ALL questions) $(8 \times 5 = 40)$ Explain different types of DC link configuration. I. (a) Explain the valve firing scheme with a neat diagram (b) Explain the following terms related to converter configuration. (c) Valve utilization factor (i) Transformer utilization factor (ii) Explain the effect of source reactance on converter circuit. (d) Explain pulse frequency control. (e) Write short notes on the following converter faults. (f) Arcthrough Misfire (ii) Explain the concept of DC circuit interruption. (g) Describe Thyristor Controlled Reactor (TCR). (h) PART B $(4 \times 15 = 60)$ (10)Explain major components of HVDC converter station using suitable diagrams. II. (a) Describe modern trends in DC transmission. (5) (b) (5) What are the different considerations taken into account in the design of valve? III. (a) (10)Compare AC and DC transmission system based on technical performance and (b) reliability. Give the applications of DC transmission system. **(7)** Explain the rectifier characteristics of a six-pulse converter. IV. (a) (8) Explain three and four valve conduction mode of a graetz circuit. (b) Explain the characteristics of a twelve-pulse bridge converter circuit. (8) V. (a) Explain the simplified analysis of graetz circuit without overlap. (b) Explain converter control characteristics. What are the two requirements which (8) VI. (a) necessitates the modification of control characteristics. Explain hierarchical control structure of a DC link. (7) (b) Describe the principles of DC link control. VII. **(7)** (a) Explain over voltages in a converter station and protection against over voltages. (8) (b) VIII. Explain potential application and types of multi terminal DC system. (10)(a) Write short notes on DC line insulation. (5) (b) Describe the philosophy and tools of HVDC system simulation. (6)IX. (a) (9)What are the sources of reactive power in a HVDC system and how it can be (b) eliminated?

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