## SELCOMP/III (R.) EDLC 1315/13

P4-RT-Exam.-Feb.-13-2-275

Con. 6402-13.

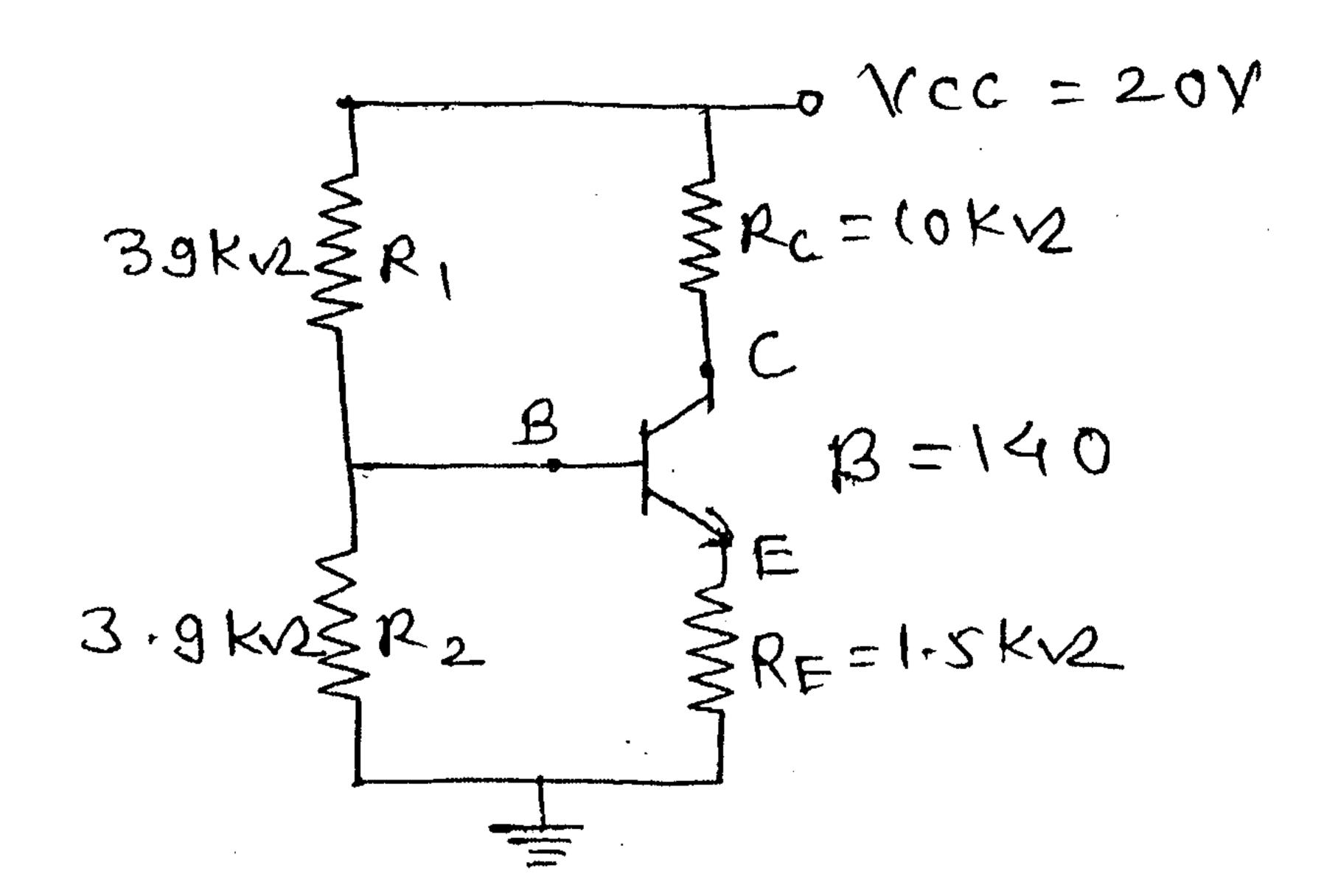
GS-6195

(3 Hours)

[Total Marks: 100

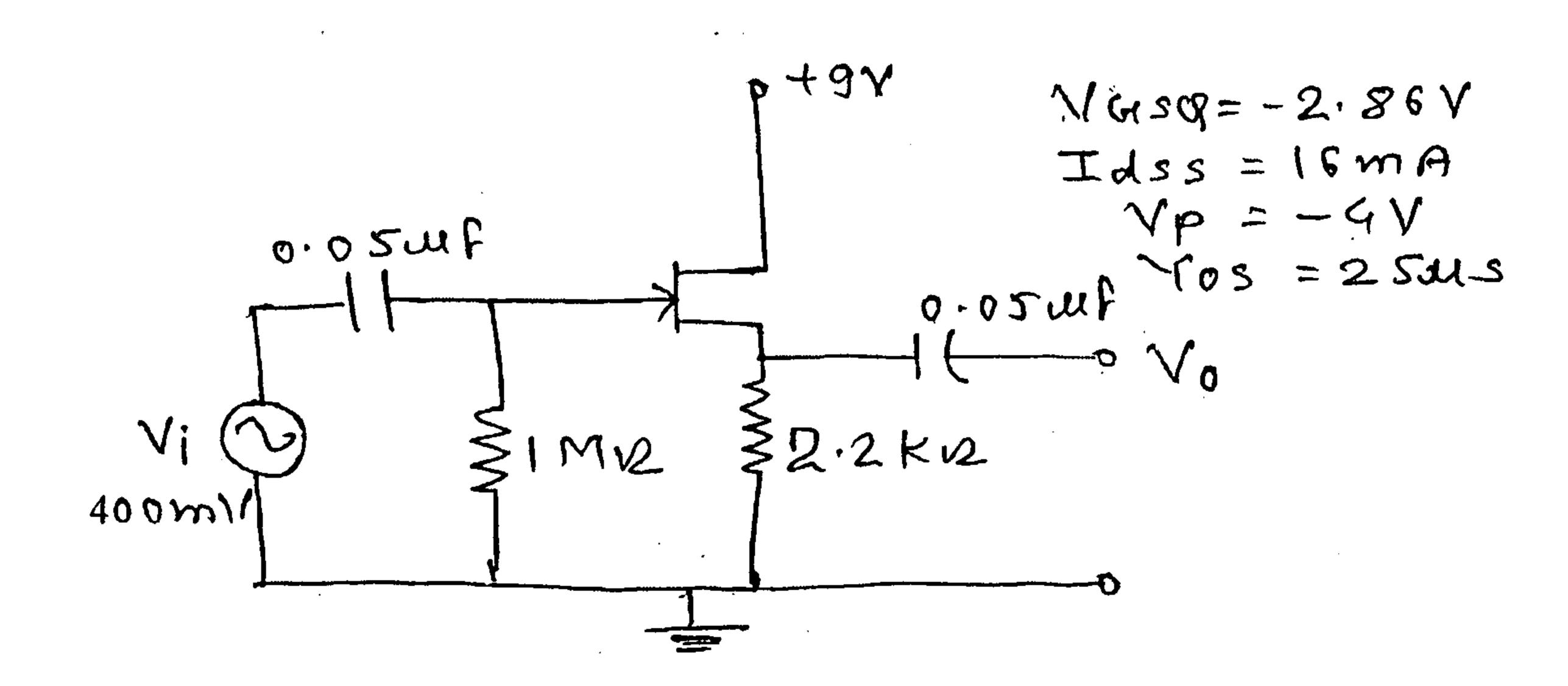
N.B.: (!) Question No. 1 is compulsory.

- (2) Attempt any four questions out of the remaining six questions.
- (3) Figures to the right indicates full marks.
- (4) Assume suitable data wherever necessary and mention it clearly.
- 1. (a) Explain the purpose of thin and lightly doped base region of BJT structure.
  - (b) List the characteristic features of 555 timer.
    - (c) Design a circuit with OP-AMP to produce the O/P  $V_0$  given by  $V_0 = (Vs_1 + Vs_3) (Vs_2 + Vs_4)$
    - (d) Justify how FET can be used as variable resistor, constant source and constant voltage source.
- 2. (a) Explain the graphical determination of the h-parameters using characteristic curve 10 of CE amplifier.
  - (b) Determine the following for the circuit shown in figure:—
    - (i)  $I_{BQ}$  (ii)  $I_{CQ}$  (iii)  $V_{CEQ}$  (iv)  $V_{CQ}$  (v)  $V_{EQ}$  (vi)  $V_{BQ}$  Use exact and approximate analysis to solve the same.



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- 3. (a) Derive equations for Zi, Zo, Av, for common source configuration using voltage 10 divider network (with unbypassed Rs).
  - (b) Calculate the voltage gain, input and O/P impedance for the circuit shown below: 10



- 4. (a) Explain any two applications of monostable multivibrator using IC 555. 10
  - (b) Design a regulator using LM 723 for  $V_0 = 9$  V and  $I_0 = 3$  Amps. 10
- 5. (a) Explain the D/A converter using binary weighted resistor.
  - (b) Explain how an op-amp can be used as (i) Integrator, (ii) Differentiator. 10
- 6. (a) Explain Instrumentation amplifier with three OP-AMPs and derive overall gain Av. 10
  - (b) Using IC 555 design Astable multivibrator for output frequency at 5 kHz and duty 10 cycle of 70%. Draw the related waveforms.

**20** 

- 7. Write short notes on (any four):-
  - (a) Virtual ground concept of OP-AMP
  - (b) Inverting Schmitt Trigger
  - (c) Properties of Ideal OP-AMP
  - (d) PLL
  - (e) Zero crossing detector.