Roll No ....

EC-5003 (CBGS)

B.E. V Semester

Examination, November 2018

Choice Based Grading System (CBGS)

Microprocessor and Microcontroller

Time : Three Hours

Maximum Marks : 70

Note: i)　Attempt any five questions.

ii)　All questions carry equal marks.

1. a)　With the help of block diagram explain the working and architecture of bus interface unit.

   b)　Discuss the memory organisation of 8086 microprocessor.

2. a)　Explain the architecture and working of 8288 bus controller.

   b)　Briefly discuss different addressing modes of 8086.

3. Discuss in detail about the ALP tools.

4. a)　Discuss about coprocessor 8087 NDP.

   b)　What do you understand by Lightly and Tightly coupled system?

5. a)　Explain the architecture and working of 8255.

   b)　Explain about programmable communication interface 8251 USART.

6. a)　Explain different types of 8086 interrupts.

   b)　Explain the basic operation of DMA controller 8257.

7. With the help of block diagram explain the architecture, features, memory organisation and addressing modes of 8051.

8. Write short notes on any two of the following:

   a)　Clock generator 8284

   b)　Programmable interval timer 8253

   c)　Programmable interrupt controller 8259

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