



M 26233

Reg. No. :

Name :

**V Semester B.Tech. Degree (Reg./Sup./Imp. – Including Part Time)
Examination, November 2014
(2007 Admn. Onwards)
PT2K6/2K6 EC 504 : COMPUTER ORGANIZATION AND
ARCHITECTURE**

Time : 3 Hours

Max. Marks : 100

PART – A

Answer **all** questions.

1. a) How will you measure the performance of a system ? Explain how performance can be improved.
- b) What is a subroutine ? Explain the necessary steps needed to perform when a subroutine is called.
- c) Explain instruction execution cycle.
- d) Explain control sequence of an unconditional branch instruction.
- e) Write notes on semiconductor memories.
- f) What is virtual memory ? Explain its organization.
- g) List and explain the various registers associated with I/O interface.
- h) What are vectored interrupts ? **(8×5=40)**

PART – B

Answer **any one** question from the **two** choices given. **(4×15=60)**

2. a) What are addressing modes ? Explain the different addressing modes with example. **10**
- b) Explain any two arithmetic operation on floating point numbers. **5**

OR

P.T.O.