

## UNIT 3 INTERMEDIATE CODE GENERATION

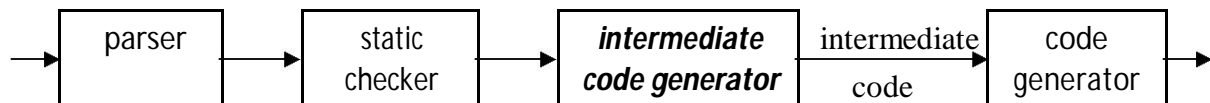
### 3.1 INTRODUCTION

The front end translates a source program into an intermediate representation from which the back end generates target code.

**Benefits of using a machine-independent intermediate form are:**

1. Retargeting is facilitated. That is, a compiler for a different machine can be created by attaching a back end for the new machine to an existing front end.
2. A machine-independent code optimizer can be applied to the intermediate representation.

#### *Position of intermediate code generator*



### 3.2 INTERMEDIATE LANGUAGES

Three ways of intermediate representation:

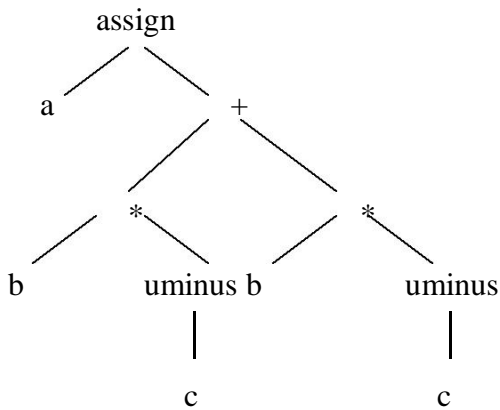
- Syntax tree
- Postfix notation
- Three address code

The semantic rules for generating three-address code from common programming language constructs are similar to those for constructing syntax trees or for generating postfix notation.

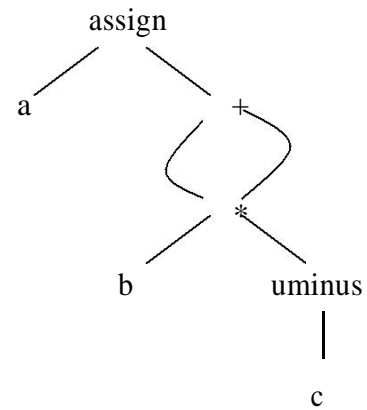
**Graphical Representations:**

**Syntax tree:**

A syntax tree depicts the natural hierarchical structure of a source program. A **dag(Directed Acyclic Graph)** gives the same information but in a more compact way because common subexpressions are identified. A syntax tree and dag for the assignment statement **a := b \* - c + b \* - c** are as follows:



(a) Syntax tree



(b) Dag

## Postfix notation:

Postfix notation is a linearized representation of a syntax tree; it is a list of the nodes of the tree in which a node appears immediately after its children. The postfix notation for the syntax tree given above is

a b c uminus \* b c uminus \* + assign

## Syntax-directed definition:

Syntax trees for assignment statements are produced by the syntax-directed definition. Non-terminal S generates an assignment statement. The two binary operators + and \* are examples of the full operator set in a typical language. Operator associativities and precedences are the usual ones, even though they have not been put into the grammar. This definition constructs the tree from the input  $a := b * - c + b * - c$ .

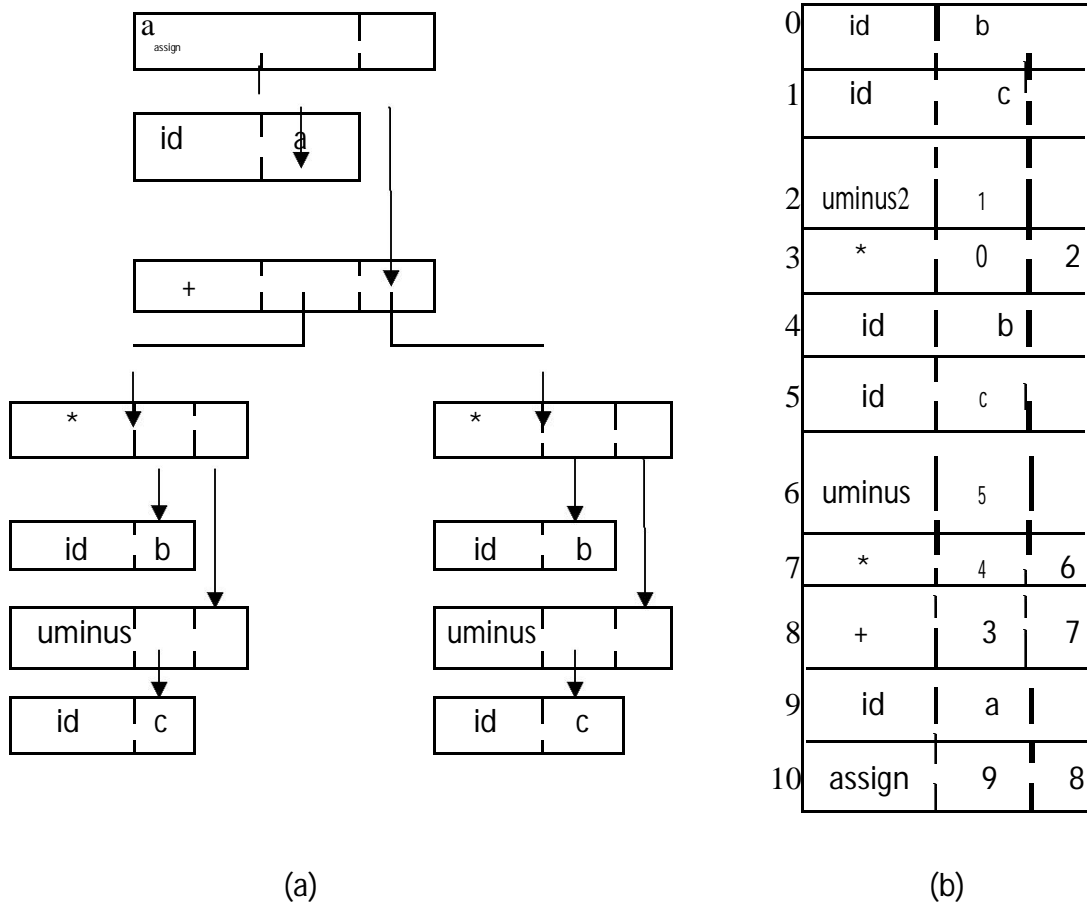
PRODUCTION	SEMANTIC RULE
$S \rightarrow id := E$	$S.nptr := mknode('assign', mkleaf(id, id.place), E.nptr)$
$E \rightarrow E_1 + E_2$	$E.nptr := mknode('+', E_1.nptr, E_2.nptr)$
$E \rightarrow E_1 * E_2$	$E.nptr := mknode('*', E_1.nptr, E_2.nptr)$
$E \rightarrow - E_1$	$E.nptr := mknode('uminus', E_1.nptr)$
$E \rightarrow (E_1)$	$E.nptr := E_1.nptr$
$E \rightarrow id$	$E.nptr := mkleaf(id, id.place)$

**Syntax-directed definition to produce syntax trees for assignment statements**

The token **id** has an attribute *place* that points to the symbol-table entry for the identifier. A symbol-table entry can be found from an attribute **id.name**, representing the lexeme associated with that occurrence of **id**. If the lexical analyzer holds all lexemes in a single array of characters, then attribute *name* might be the index of the first character of the lexeme.

Two representations of the syntax tree are as follows. In (a) each node is represented as a record with a field for its operator and additional fields for pointers to its children. In (b), nodes are allocated from an array of records and the index or position of the node serves as the pointer to the node. All the nodes in the syntax tree can be visited by following pointers, starting from the root at position 10.

**Two representations of the syntax tree**



## Three-Address Code:

Three-address code is a sequence of statements of the general form

$$x := y \text{ op } z$$

where  $x$ ,  $y$  and  $z$  are names, constants, or compiler-generated temporaries;  $op$  stands for any operator, such as a fixed- or floating-point arithmetic operator, or a logical operator on boolean-valued data. Thus a source language expression like  $x + y * z$  might be translated into a sequence

$$\begin{aligned} t_1 &:= y * z \\ x &:= x + t_1 \end{aligned}$$

where  $t_1$  and  $t_2$  are compiler-generated temporary names.

### Advantages of three-address code:

- The unraveling of complicated arithmetic expressions and of nested flow-of-control statements makes three-address code desirable for target code generation and optimization.
- The use of names for the intermediate values computed by a program allows three-address code to be easily rearranged – unlike postfix notation.

Three-address code is a linearized representation of a syntax tree or a dag in which explicit names correspond to the interior nodes of the graph. The syntax tree and dag are represented by the three-address code sequences. Variable names can appear directly in three-address statements.

### Three-address code corresponding to the syntax tree and dag given above

$t_1 := -c$  $t_2 := b * t_1$  $t_3 := -c$  $t_4 := b * t_3$  $t_5 := t_2 + t_4$  $a := t_5$	$t_1 := -c$  $t_2 := b * t_1$  $t_5 := t_2 + t_2$  $a := t_5$
--	---

#### (a) Code for the syntax tree

#### (b) Code for the dag

The reason for the term “three-address code” is that each statement usually contains three addresses, two for the operands and one for the result.

### Types of Three -Address Statements:

The common three-address statements are:

1. Assignment statements of the form  $x := yopz$ , where  $op$  is a binary arithmetic or logical operation.
2. Assignment instructions of the form  $x := opy$ , where  $op$  is a unary operation. Essential unary

operations include unary minus, logical negation, shift operators, and conversion operators that, for example, convert a fixed-point number to a floating-point number.

3. *Copy statements* of the form  $x := y$  where the value of  $y$  is assigned to  $x$ .
4. The unconditional jump *goto*  $L$ . The three-address statement with label  $L$  is the next to be executed.
5. Conditional jumps such as **if**  $x$  *relop*  $y$  **goto**  $L$ . This instruction applies a relational operator ( $<$ ,  $=$ ,  $>$ , etc.) to  $x$  and  $y$ , and executes the statement with label  $L$  next if  $x$  stands in relation *relop* to  $y$ . If not, the three-address statement following if  $x$  *relop*  $y$  **goto**  $L$  is executed next, as in the usual sequence.
6. *param*  $x$  and *call*  $p, n$  for procedure calls and *return*  $y$ , where  $y$  representing a returned value is optional. For example,
 

```

param
x1
param
x2
...
param
xn  call
p,n
      
```

 generated as part of a call of the procedure  $p(x_1, x_2, \dots, x_n)$ .
7. Indexed assignments of the form  $x := y[i]$  and  $x[i] := y$ .
8. Address and pointer assignments of the form  $x := \&y$ ,  $x := *y$ , and  $*x := y$ .

### 3.3 SYNTAX-DIRECTED TRANSLATION INTO THREE-ADDRESS CODE:

When three-address code is generated, temporary names are made up for the interior nodes of a syntax tree. For example, **id**  $:= E$  consists of code to evaluate  $E$  into some temporary  $t$ , followed by the assignment **id.place**  $:= t$ .

Given input  $a := b * -c + b * -c$ , the three-address code is as shown above. The synthesized attribute  $S.code$  represents the three-address code for the assignment  $S$ . The nonterminal  $E$  has two attributes :

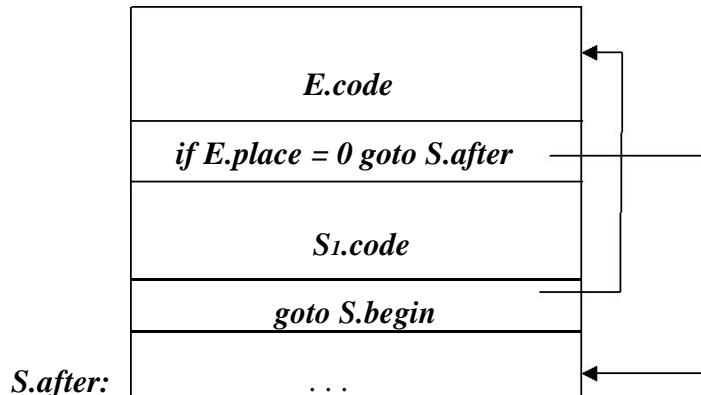
1.  $E.place$ , the name that will hold the value of  $E$ , and
2.  $E.code$ , the sequence of three-address statements evaluating  $E$ .

## Syntax -directed definition to produce three-address code for assignments

PRODUCTION	SEMANTIC RULES
$S \rightarrow id := E$	$S.code := E.code \parallel gen(id.place \text{ ':=' } E.place)$
$E \rightarrow E_1 + E_2$	$E.place := newtemp;$ $E.code := E_1.code \parallel E_2.code \parallel gen(E.place \text{ ':=' } E_1.place \text{ '+' } E_2.place)$
$E \rightarrow E_1 * E_2$	$E.place := newtemp;$ $E.code := E_1.code \parallel E_2.code \parallel gen(E.place \text{ ':=' } E_1.place \text{ '*' } E_2.place)$
$E \rightarrow - E_1$	$E.place := newtemp;$ $E.code := E_1.code \parallel gen(E.place \text{ ':=' 'uminus' } E_1.place)$
$E \rightarrow (E_1)$	$E.place := E_1.place;$ $E.code := E_1.code$
$E \rightarrow id$	$E.place := id.place;$ $E.code := \text{' '}$

## Semantic rules generating code for a while statement

*S.begin:*



### PRODUCTION

$S \rightarrow \text{while } E \text{ do } S_1$

### SEMANTIC RULES

$S.begin := newlabel;$   
 $S.after := newlabel;$   
 $S.code := gen(S.begin \text{ ':'}) \parallel$   
 $E.code \parallel$   
 $gen ( \text{'if' } E.place \text{ '=' '0' 'goto' } S.after) \parallel$   
 $S_1.code \parallel$   
 $gen ( \text{'goto' } S.begin) \parallel$   
 $gen (S.after \text{ ':'})$

- The function *newtemp* returns a sequence of distinct names  $t_1, t_2, \dots$  in response to successive calls.
- Notation  $gen(x \text{ ':=' } y \text{ '+' } z)$  is used to represent three-address statement  $x := y + z$ . Expressions appearing instead of variables like  $x$ ,  $y$  and  $z$  are evaluated when passed to *gen*, and quoted operators or operand, like '+' are taken literally.
- Flow-of-control statements can be added to the language of assignments. The code for  $S \rightarrow \text{while } E \text{ do } S_1$  is generated using new attributes *S.begin* and *S.after* to mark the first statement in the code for  $E$  and the statement following the code for  $S$ , respectively.
- The function *newlabel* returns a new label every time it is called.
- We assume that a non-zero expression represents true; that is when the value of  $E$  becomes zero, control leaves the while statement.

### 3.4 IMPLEMENTATION OF THREE-ADDRESS STATEMENTS:

A three-address statement is an abstract form of intermediate code. In a compiler, these statements can be implemented as records with fields for the operator and the operands. Three such representations are:

- Quadruples
- Triples
- Indirect triples

#### *Quadruples:*

- A quadruple is a record structure with four fields, which are, *op*, *arg1*, *arg2* and *result*.
- The *op* field contains an internal code for the operator. The three-address statement  $x := y \text{ op } z$  is represented by placing  $y$  in *arg1*,  $z$  in *arg2* and  $x$  in *result*.
- The contents of fields *arg1*, *arg2* and *result* are normally pointers to the symbol-table entries for the names represented by these fields. If so, temporary names must be entered into the symbol table as they are created.

#### *Triples:*

- To avoid entering temporary names into the symbol table, we might refer to a temporary value by the position of the statement that computes it.
- If we do so, three-address statements can be represented by records with only three
- The fields *arg1* and *arg2*, for the arguments of *op*, are either pointers to the symbol table or pointers into the triple structure ( for temporary values ).

- Since three fields are used, this intermediate code format is known as *triples*.

	<i>op</i>	<i>arg1</i>	<i>arg2</i>	<i>result</i>
(0)	uminus	c		t1
(1)	*	b	t1	t2
(2)	uminus	c		t3
(3)	*	b	t3	t4
(4)	+	t2	t4	t5
(5)	:=	t3		a

**(a) Quadruples**

	<i>op</i>	<i>arg1</i>	<i>arg2</i>
(0)	uminus	c	
(1)	*	b	(0)
(2)	uminus	c	
(3)	*	b	(2)
(4)	+	(1)	(3)
(5)	assign	a	(4)

**(b) Triples**

### Quadruple and triple representation of three-address statements given above:

A ternary operation like  $x[i] := y$  requires two entries in the triple structure as shown as below while  $x := y[i]$  is naturally represented as two operations.

**(a)  $x[i] := y$**

	<i>op</i>	<i>arg1</i>	<i>arg2</i>
(0)	[ ] =	x	i
(1)	assign	(0)	y

**(b)  $x := y[i]$**

	<i>op</i>	<i>arg1</i>	<i>arg2</i>
(0)	= [ ]	y	I
(1)	assign	x	(0)

- fields: *op*, *arg1* and *arg2*.

### **Indirect Triples:**

- Another implementation of three-address code is that of listing pointers to triples, rather than listing the triples themselves. This implementation is called indirect triples.
- For example, let us use an array statement to list pointers to triples in the desired order. Then the triples shown above might be represented as follows:



	<i>statement</i>		<i>op</i>	<i>arg1</i>	<i>arg2</i>
(0)	(14)	(14)	uminus	c	
(1)	(15)	(15)	*	b	(14)
(2)	(16)	(16)	uminus	c	
(3)	(17)	(17)	*	b	(16)
(4)	(18)	(18)	+	(15)	(17)
(5)	(19)	(19)	assign	a	(18)

## Indirect triples representation of three-address statements

### 3.5 DECLARATIONS

As the sequence of declarations in a procedure or block is examined, we can lay out storage for names local to the procedure. For each local name, we create a symbol-table entry with information like the type and the relative address of the storage for the name. The relative address consists of an offset from the base of the static data area or the field for local data in an activation record.

#### Declarations in a Procedure:

The syntax of languages such as C, Pascal and Fortran, allows all the declarations in a single procedure to be processed as a group. In this case, a global variable, say *offset*, can keep track of the next available relative address.

In the translation scheme shown below:

- Nonterminal P generates a sequence of declarations of the form **id :T**.
- Before the first declaration is considered, *offset* is set to 0. As each new name is seen, that name is entered in the symbol table with offset equal to the current value of *offset*, and *offset* is incremented by the width of the data object denoted by that name.
- The procedure *enter( name, type, offset )* creates a symbol-table entry for *name*, gives its type *type* and relative address *offset* in its data area.
- Attribute *type* represents a type expression constructed from the basic types *integer* and *real* by applying the type constructors *pointer* and *array*. If type expressions are represented by graphs, then attribute *type* might be a pointer to the node representing a type expression.
- The width of an array is obtained by multiplying the width of each element by the number of elements in the array. The width of each pointer is assumed to be 4.

## Computing the types and relative addresses of declared names

$P \rightarrow D$	$\{ offset : = 0 \}$
$D \rightarrow D ; D$	
$D \rightarrow id : T$	$\{ enter(id.name, T.type, offset);$ $offset : = offset + T.width \}$
$T \rightarrow integer$	$\{ T.type : = integer;$ $T.width : = 4 \}$
$T \rightarrow real$	$\{ T.type : = real;$ $T.width : = 8 \}$
$T \rightarrow array [ num ] \text{ of } T_1$	$\{ T.type : = array(num.val, T_1.type);$ $T.width : = num.val \times T_1.width \}$
$T \rightarrow \uparrow T_1$	$\{ T.type : = pointer ( T_1.type);$ $T.width : = 4 \}$

**Keeping Track of Scope Information:**

When a nested procedure is seen, processing of declarations in the enclosing procedure is temporarily suspended. This approach will be illustrated by adding semantic rules to the following language:

$P \rightarrow D$

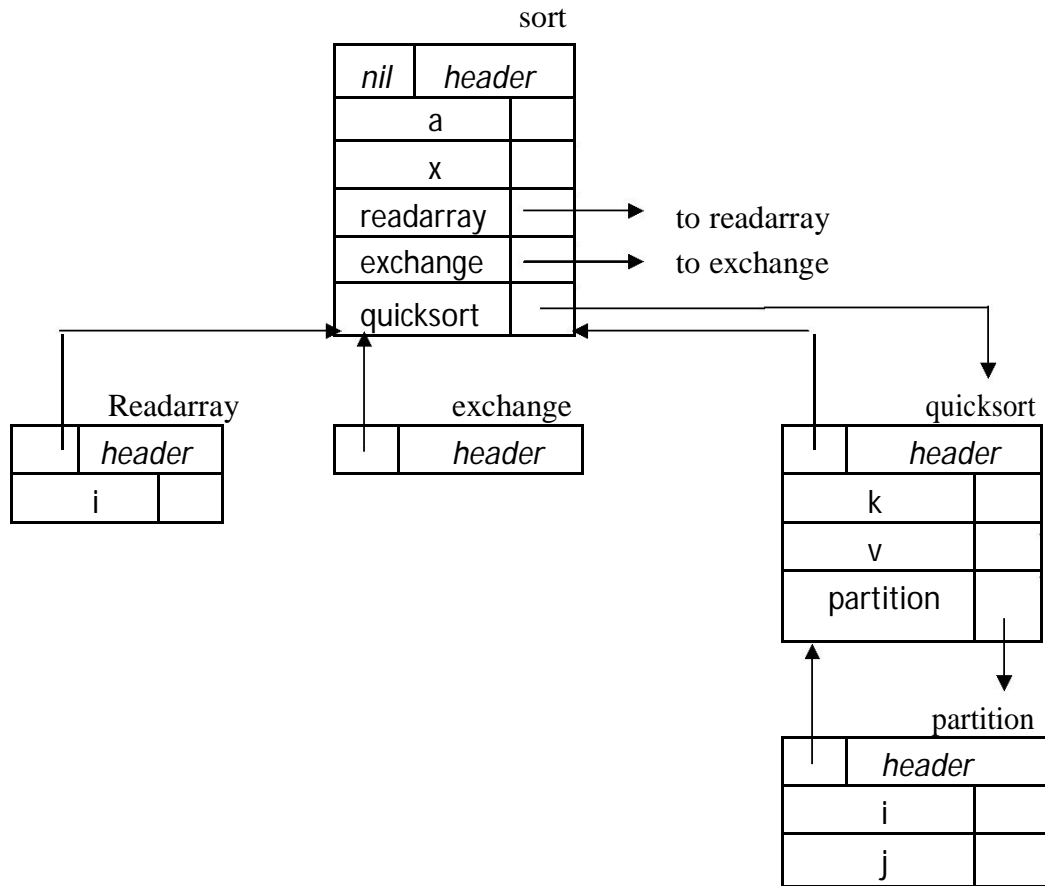
$D \rightarrow D ; D / id : T / \text{proc } id ; D ; S$

One possible implementation of a symbol table is a linked list of entries for names.

A new symbol table is created when a procedure declaration  $D \rightarrow \text{proc } id D_1 ; S$  is seen, and entries for the declarations in  $D_1$  are created in the new table. The new table points back to the symbol table of the enclosing procedure; the name represented by  $id$  itself is local to the enclosing procedure. The only change from the treatment of variable declarations is that the procedure *enter* is told which symbol table to make an entry in.

For example, consider the symbol tables for procedures *readarray*, *exchange*, and *quicksort* pointing back to that for the containing procedure *sort*, consisting of the entire program. Since *partition* is declared within *quicksort*, its table points to that of *quicksort*.

## Symbol tables for nested procedures



The semantic rules are defined in terms of the following operations:

1. *mktable(previous)* creates a new symbol table and returns a pointer to the new table. The argument *previous* points to a previously created symbol table, presumably that for the enclosing procedure.
2. *enter(table, name, type, offset)* creates a new entry for name *name* in the symbol table pointed to by *table*. Again, *enter* places type *type* and relative address *offset* in fields within the entry.
3. *addwidth(table, width)* records the cumulative width of all the entries in table in the header associated with this symbol table.
4. *enterproc(table, name, newtable)* creates a new entry for procedure *name* in the symbol table pointed to by *table*. The argument *newtable* points to the symbol table for this procedure *name*.

## Syntax directed translation scheme for nested procedures

$P \rightarrow M D$	$\{ \text{addwidth} ( \text{top} ( \text{tblptr} ) , \text{top} ( \text{offset} ) );$ $\text{pop} ( \text{tblptr} ); \text{pop} ( \text{offset} ) \}$
$M \rightarrow \varepsilon$	$\{ t := \text{mktable} ( \text{nil} );$ $\text{push} ( t, \text{tblptr} ); \text{push} ( 0, \text{offset} ) \}$
$D \rightarrow D_1 ; D_2$	
$D \rightarrow \text{proc id} ; N D_1 ; S$	$\{ t := \text{top} ( \text{tblptr} );$ $\text{addwidth} ( t, \text{top} ( \text{offset} ) );$ $\text{pop} ( \text{tblptr} ); \text{pop} ( \text{offset} );$ $\text{enterproc} ( \text{top} ( \text{tblptr} ), \text{id.name}, t ) \}$
$D \rightarrow \text{id} : T$	$\{ \text{enter} ( \text{top} ( \text{tblptr} ), \text{id.name}, T.\text{type}, \text{top} ( \text{offset} ) );$ $\text{top} ( \text{offset} ) := \text{top} ( \text{offset} ) + T.\text{width} \}$
$N \rightarrow \varepsilon$	$\{ t := \text{mktable} ( \text{top} ( \text{tblptr} ) );$ $\text{push} ( t, \text{tblptr} ); \text{push} ( 0, \text{offset} ) \}$

- The stack *tblptr* is used to contain pointers to the tables for **sort**, **quicksort**, and **partition** when the declarations in **partition** are considered.
- The top element of stack *offset* is the next available relative address for a local of the current procedure.
- All semantic actions in the subtrees for B and C in

$$A \rightarrow BC \{ \text{action}_A \}$$

are done before *action<sub>A</sub>* at the end of the production occurs. Hence, the action associated with the marker M is the first to be done.

- The action for nonterminal M initializes stack *tblptr* with a symbol table for the outermost scope, created by operation *mktable(nil)*. The action also pushes relative address 0 onto stack *offset*.
- Similarly, the nonterminal N uses the operation *mktable(top(tblptr))* to create a new symbol table. The argument *top(tblptr)* gives the enclosing scope for the new table.
- For each variable declaration **id: T**, an entry is created for **id** in the current symbol table. The top of stack *offset* is incremented by T.width.
- When the action on the right side of  $D \rightarrow \text{proc id} ; N D_1 ; S$  occurs, the width of all

declarations generated by  $D_1$  is on the top of stack offset; it is recorded using *addwidth*. Stacks *tblptr* and *offset* are then popped.

At this point, the name of the enclosed procedure is entered into the symbol table of its enclosing procedure.

## 3.6 ASSIGNMENT STATEMENTS

Suppose that the context in which an assignment appears is given by the following grammar.

$$P \rightarrow M D$$

$$M \rightarrow \varepsilon$$

$$D \rightarrow D ; D \mid \text{id} : T \mid \text{proc id} ; N D ; S$$

$$N \rightarrow \varepsilon$$

Nonterminal  $P$  becomes the new start symbol when these productions are added to those in the translation scheme shown below.

### Translation scheme to produce three-address code for assignments

$$\begin{aligned}
 S \rightarrow \text{id} := E \quad & \{ p := \text{lookup}(\text{id.name}); \\
 & \quad \text{if } p \neq \text{nil} \text{ then} \\
 & \quad \quad \text{emit}(p := E.\text{place}) \\
 & \quad \text{else error} \} \\
 E \rightarrow E_1 + E_2 \quad & \{ E.\text{place} := \text{newtemp}; \\
 & \quad \text{emit}(E.\text{place} := E_1.\text{place} + E_2.\text{place}) \} \\
 E \rightarrow E_1 * E_2 \quad & \{ E.\text{place} := \text{newtemp}; \\
 & \quad \text{emit}(E.\text{place} := E_1.\text{place} * E_2.\text{place}) \} \\
 E \rightarrow - E_1 \quad & \{ E.\text{place} := \text{newtemp}; \\
 & \quad \text{emit}(E.\text{place} := \text{'uminus'} E_1.\text{place}) \} \\
 E \rightarrow ( E_1 ) \quad & \{ E.\text{place} := E_1.\text{place} \} \\
 E \rightarrow \text{id} \quad & \{ p := \text{lookup}(\text{id.name}); \\
 & \quad \text{if } p \neq \text{nil} \text{ then} \\
 & \quad \quad E.\text{place} := p \\
 & \quad \text{else error} \\
 & \}
 \end{aligned}$$

## Reusing Temporary Names

- The temporaries used to hold intermediate values in expression calculations tend to clutter up the symbol table, and space has to be allocated to hold their values.

- Temporaries can be reused by changing *newtemp*. The code generated by the rules for E

→  $E_1 + E_2$  has the general form:

evaluate  $E_1$  into  $t_1$

evaluate  $E_2$  into  $t_2$

$t := t_1 + t_2$

- The lifetimes of these temporaries are nested like matching pairs of balanced parentheses.
- Keep a count  $c$ , initialized to zero. Whenever a temporary name is used as an operand, decrement  $c$  by 1. Whenever a new temporary name is generated, use  $\$c$  and increase  $c$  by 1.
- For example, consider the assignment  $x := a * b + c * d - e * f$

### Three-address code with stack temporaries

<i>statement</i>	<i>value of c</i>
	0
$\$0 := a * b$	1
$\$1 := c * d$	2
$\$0 := \$0 + \$1$	1
$\$1 := e * f$	2
$\$0 := \$0 - \$1$	1
$x := \$0$	0

## Addressing Array Elements:

Elements of an array can be accessed quickly if the elements are stored in a block of consecutive locations. If the width of each array element is  $w$ , then the  $i$ th element of array  $A$  begins in location

$$base + (i - low) \times w$$

where  $low$  is the lower bound on the subscript and  $base$  is the relative address of the storage allocated for the array. That is,  $base$  is the relative address of  $A[low]$ .

The expression can be partially evaluated at compile time if it is rewritten as

$$i \times w + (base - low \times w)$$

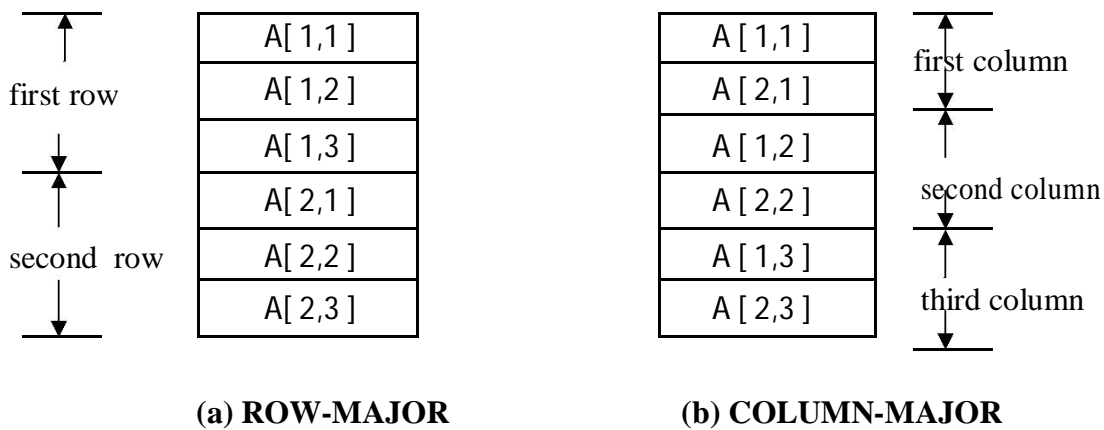
The subexpression  $c = \text{base} - \text{low} \times w$  can be evaluated when the declaration of the array is seen. Let us assume that  $c$  is saved in the symbol table entry for  $A$ , so the relative address of  $A[i]$  is obtained by simply adding  $i \times w$  to  $c$ .

### Address calculation of multi-dimensional arrays:

A two-dimensional array is stored in of the two forms :

- Row-major (row-by-row)
- Column-major (column-by-column)

### Layouts for a 2 x 3 array



In the case of row-major form, the relative address of  $A[i_1, i_2]$  can be calculated by the formula

$$\text{base} + ((i_1 - \text{low}_1) \times n_2 + i_2 - \text{low}_2) \times w$$

where,  $\text{low}_1$  and  $\text{low}_2$  are the lower bounds on the values of  $i_1$  and  $i_2$  and  $n_2$  is the number of values that  $i_2$  can take. That is, if  $\text{high}_2$  is the upper bound on the value of  $i_2$ , then  $n_2 = \text{high}_2 - \text{low}_2 + 1$ .

Assuming that  $i_1$  and  $i_2$  are the only values that are known at compile time, we can rewrite the above expression as

$$((i_1 \times n_2) + i_2) \times w + (\text{base} - ((\text{low}_1 \times n_2) + \text{low}_2) \times w)$$

### Generalized formula:

The expression generalizes to the following expression for the relative address of  $A[i_1, i_2, \dots, i_k]$

$$((\dots((i_1 n_2 + i_2) n_3 + i_3) \dots) n_k + i_k) \times w + \text{base} - ((\dots((\text{low}_1 n_2 + \text{low}_2) n_3 + \text{low}_3) \dots) n_k + \text{low}_k) \times w$$

for all  $j$ ,  $n_j = \text{high}_j - \text{low}_j + 1$

## The Translation Scheme for Addressing Array Elements :

Semantic actions will be added to the grammar :

- (1)  $S \rightarrow L : = E$
- (2)  $E \rightarrow E + E$
- (3)  $E \rightarrow ( E )$
- (4)  $E \rightarrow L$
- (5)  $L \rightarrow Elist \ ]$
- (6)  $L \rightarrow id$
- (7)  $Elist \rightarrow Elist , E$
- (8)  $Elist \rightarrow id[ E$

Let us generate a normal assignment if  $L$  is a simple name, and an indexed assignment into the location denoted by  $L$  otherwise :

- (1)  $S \rightarrow L : = E$       { **if**  $L.offset = \text{null}$  **then** /\*  $L$  is a simple **id** \*/  
                                        $emit ( L.place \ ' : = ' E.place );$   
                                       **else**  
                                        $emit ( L.place \ ' [ \ L.offset \ ' ] \ ' : = ' E.place );$  }
- (2)  $E \rightarrow E_1 + E_2$       {  $E.place : = newtemp;$   
                                        $emit ( E.place \ ' : = ' E_1.place \ ' + ' E_2.place );$  }
- (3)  $E \rightarrow ( E_1 )$       {  $E.place : = E_1.place$  }

When an array reference  $L$  is reduced to  $E$ , we want the  $r$ -value of  $L$ . Therefore we use indexing to obtain the contents of the location  $L.place [ L.offset ]$  :

- (4)  $E \rightarrow L$       { **if**  $L.offset = \text{null}$  **then** /\*  $L$  is a simple **id** \*/  
                                        $E.place : = L.place$   
                                       **else begin**  
                                        $E.place : = newtemp;$   
                                        $emit ( E.place \ ' : = ' L.place \ ' [ \ L.offset \ ' ] )$   
                                       **end }**
- (5)  $L \rightarrow Elist \ ]$       {  $L.place : = newtemp;$   
                                        $L.offset : = newtemp;$   
                                        $emit ( L.place \ ' : = ' c( Elist.array );$   
                                        $emit ( L.offset \ ' : = ' Elist.place \ ' * ' width ( Elist.array );$  }
- (6)  $L \rightarrow id$       {  $L.place := id.place;$



$L.offset := \text{null}$

(7)  $Elist \rightarrow Elist_1, E$     {  $t := \text{newtemp}$ ;  
                                    $m := Elist_1.ndim + 1$ ;  
                                    $\text{emit}(t := 'Elist_1.place *' limit$   
                                    $(Elist_1.array, m)); \text{emit}(t := 't +' E.place);$   
                                    $Elist.array := Elist_1.array$ ;  
                                    $Elist.place := t$ ;  
                                    $Elist.ndim := m$  }

(8)  $Elist \rightarrow id [ E$     {  $Elist.array := id.place$ ;  
                                    $Elist.place := E.place$ ;  
                                    $Elist.ndim := 1$  }

## Type conversion within Assignments :

Consider the grammar for assignment statements as above, but suppose there are two types – real and integer , with integers converted to reals when necessary. We have another attribute  $E.type$ , whose value is either *real* or *integer*. The semantic rule for  $E.type$  associated with the production  $E \rightarrow E + E$  is :

$E \rightarrow E + E$     {  $E.type :=$   
                                   **if**  $E_1.type = integer$  **and**  
                                    $E_2.type = integer$  **then**  $integer$   
                                   **else**  $real$  }

The entire semantic rule for  $E \rightarrow E + E$  and most of the other productions must be modified to generate, when necessary, three-address statements of the form  $x := \text{inttoreal } y$ , whose effect is to convert integer  $y$  to a real of equal value, called  $x$ .

### Semantic action for $E \rightarrow E_1 + E_2$

$E.place := \text{newtemp}$ ;  
**if**  $E_1.type = integer$  **and**  $E_2.type = integer$  **then**  
      $\text{beginemit}(E.place := 'E_1.place \text{int} +'$   
      $E_2.place); E.type := integer$   
**end**  
**else if**  $E_1.type = real$  **and**  $E_2.type = real$  **then**  
      $\text{beginemit}(E.place := 'E_1.place \text{real} +'$   
      $E_2.place); E.type := real$   
**end**  
**else if**  $E_1.type = integer$  **and**  $E_2.type = real$  **then**

```

    begin  $u := \text{newtemp};$ 
    emit(  $u := \text{'inttoreal' } E_1.\text{place};$  emit(
     $E.\text{place} := u \text{ 'real + ' } E_2.\text{place};$ 
     $E.\text{type} := \text{real}$ 
end
else if  $E_1.\text{type} = \text{real}$  and  $E_2.\text{type} = \text{integer}$  then
    begin  $u := \text{newtemp};$ 
    emit(  $u := \text{'inttoreal' } E_2.\text{place};$  emit(
     $E.\text{place} := E_1.\text{place} \text{ 'real + ' } u;$ 
     $E.\text{type} := \text{real}$ 
end
else
     $E.\text{type} := \text{type\_error};$ 

```

For example, for the input  $x := y + i * j$

assuming  $x$  and  $y$  have type *real*, and  $i$  and  $j$  have type *integer*, the output would look like

```

t1 := i int* j t3 :
= inttoreal t1 t2 :
= y real+ t3 x :=
t2

```

### 3.7 BOOLEAN EXPRESSIONS

Boolean expressions have two primary purposes. They are used to compute logical values, but more often they are used as conditional expressions in statements that alter the flow of control, such as if-then-else, or while-do statements.

Boolean expressions are composed of the boolean operators ( **and**, **or**, and **not** ) applied to elements that are boolean variables or relational expressions. Relational expressions are of the form  $E_1 \text{relop} E_2$ , where  $E_1$  and  $E_2$  are arithmetic expressions.

Here we consider boolean expressions generated by the following grammar :

$$E \rightarrow E \text{ or } E \mid E \text{ and } E \mid \text{not } E \mid ( E ) \mid \text{id rel op id} \mid \text{true} \mid \text{false}$$

#### Methods of Translating Boolean Expressions:

There are two principal methods of representing the value of a boolean expression. They are :

- To encode true and false *numerically* and to evaluate a boolean expression analogously to an arithmetic expression. Often, 1 is used to denote true and 0 to denote false.
- To implement boolean expressions by *flow of control*, that is, representing the value of a boolean expression by a position reached in a program. This method is particularly

convenient in implementing the boolean expressions in flow-of-control statements, such as the if-then and while-do statements.

## Numerical Representation

Here, 1 denotes true and 0 denotes false. Expressions will be evaluated completely from left to right, in a manner similar to arithmetic expressions.

For example :

- The translation for

a **or** b **and** not c is

the three-address sequence

t<sub>1</sub> := **not** c t<sub>2</sub>

:= b **and** t<sub>1</sub> t<sub>3</sub>

:= a **or** t<sub>3</sub>

- A relational expression such as a < b is equivalent to the conditional statement if a < b then 1 else 0

which can be translated into the three-address code sequence (again, we arbitrarily start statement numbers at 100) :

100 : if a < b goto 103

101 : t := 0

102 : goto 104

103 : t := 1

104 :

## Translation scheme using a numerical representation for booleans

$E \rightarrow E_1 \text{or} E_2$	$\{ E.place := newtemp;$ $emit( E.place ':=' E_1.place \text{'or'} E_2.place ));$
$E \rightarrow E_1 \text{and} E_2$	$\{ E.place := newtemp;$ $emit( E.place ':=' E_1.place \text{'and'} E_2.place ));$
$E \rightarrow \text{not } E_1$	$\{ E.place := newtemp;$ $emit( E.place ':=' \text{'not'} E_1.place ));$
$E \rightarrow ( E_1 )$	$\{ E.place := E_1.place \}$
$E \rightarrow id_1 \text{relop } id_2$	$\{ E.place := newtemp;$ $emit( \text{'if'} id_1.place \text{relop.op } id_2.place \text{'goto'} nextstat + 3);$ $emit( E.place ':=' \text{'0'} );$ $emit( \text{'goto'} nextstat + 2);$ $emit( E.place ':=' \text{'1'} );$
$E \rightarrow \text{true}$	$\{ E.place := newtemp;$ $emit( E.place ':=' \text{'1'} );$
$E \rightarrow \text{false}$	$\{ E.place := newtemp;$ $emit( E.place ':=' \text{'0'} );$

## Short-Circuit Code:

It is also possible to translate a boolean expression into three-address code without generating code for any of the boolean operators and without having the code necessarily evaluate the entire expression. This style of evaluation is sometimes called “**short-circuit**” or “**jumping**” code. It is possible to evaluate boolean expressions without generating code for the boolean operators **and**, **or**, and **not** if we represent the value of an expression by a position in the code sequence.

### Translation of $a < b$ or $c < d$ and $e < f$

100 : if $a < b$ goto 103	107 : $t_2 := 1$
101 : $t_1 := 0$	108 : if $e < f$ goto 111
102 : goto 104	109 : $t_3 := 0$
103 : $t_1 := 1$	110 : goto 112
104 : if $c < d$ goto 107	111 : $t_3 := 1$
105 : $t_2 := 0$	112 : $t_4 := t_2$ and $t_3$
106 : goto 108	113 : $t_5 := t_1$ or $t_4$

## Flow-of-Control Statements

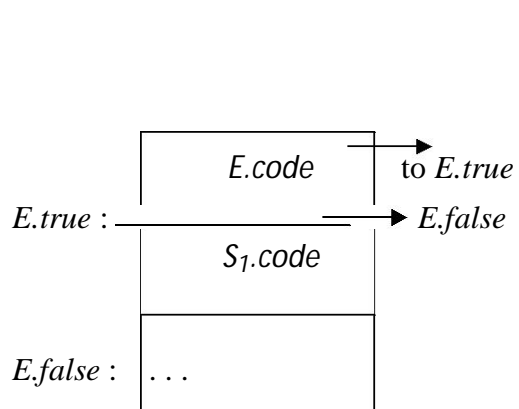
Let us consider the translation of boolean expressions into three-address code in the context of if-then, if-then-else, and while-do statements such as those generated by the following grammar:

$S \rightarrow \text{if } E \text{ then } S_1$   
 $\quad | \text{ if } E \text{ then } S_1 \text{ else } S_2$   
 $\quad | \text{ while } E \text{ do } S_1$

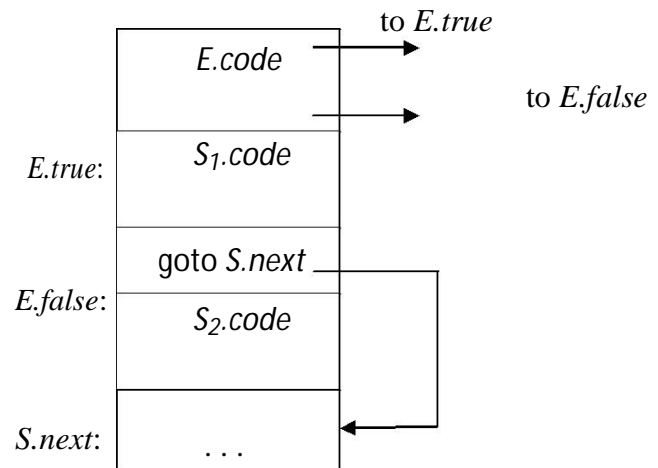
In each of these productions,  $E$  is the Boolean expression to be translated. In the translation, we assume that a three-address statement can be symbolically labeled, and that the function *newlabel* returns a new symbolic label each time it is called.

- $E.\text{true}$  is the label to which control flows if  $E$  is true, and  $E.\text{false}$  is the label to which control flows if  $E$  is false.
- The semantic rules for translating a flow-of-control statement  $S$  allow control to flow from the translation  $S.\text{code}$  to the three-address instruction immediately following  $S.\text{code}$ .
- $S.\text{next}$  is a label that is attached to the first three-address instruction to be executed after the code for  $S$ .

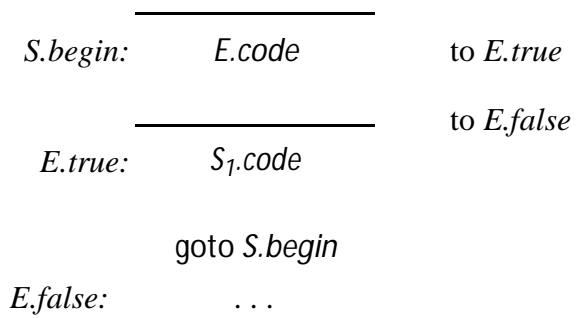
## Code for if-then , if-then-else, and while-do statements



(a) if-then



(b) if-then-else



(c) while-do

## Syntax-directed definition for flow-of-control statements

PRODUCTION	SEMANTIC RULES
$S \rightarrow \text{if } E \text{ then } S_1$	$E.true := \text{newlabel};$ $E.false := S.next;$ $S_1.next := S.next;$ $S.code := E.code \parallel \text{gen}(E.true ':') \parallel S_1.code$
$S \rightarrow \text{if } E \text{ then } S_1 \text{ else } S_2$	$E.true := \text{newlabel};$ $E.false := \text{newlabel};$ $S_1.next := S.next;$ $S_2.next := S.next;$ $S.code := E.code \parallel \text{gen}(E.true ':') \parallel S_1.code \parallel$ $\text{gen}(\text{'goto' } S.next) \parallel$ $\text{gen}(E.false ':') \parallel S_2.code$
$S \rightarrow \text{while } E \text{ do } S_1$	$S.begin := \text{newlabel};$ $E.true := \text{newlabel};$ $E.false := S.next;$ $S_1.next := S.begin;$ $S.code := \text{gen}(S.begin ':') \parallel E.code \parallel$ $\text{gen}(E.true ':') \parallel S_1.code \parallel$ $\text{gen}(\text{'goto' } S.begin)$

## Control-Flow Translation of Boolean Expressions:

### Syntax-directed definition to produce three-address code for booleans

PRODUCTION	SEMANTIC RULES
$E \rightarrow E_1 \text{ or } E_2$	$E_1.true := E.true;$ $E_1.false := \text{newlabel};$ $E_2.true := E.true;$ $E_2.false := E.false;$ $E.code := E_1.code \parallel \text{gen}(E_1.false ':') \parallel E_2.code$
$E \rightarrow E_1 \text{ and } E_2$	$E.true := \text{newlabel};$ $E_1.false := E.false;$ $E_2.true := E.true;$ $E_2.false := E.false;$

$E \rightarrow \text{not } E_1$	$E.code := E_1.code \parallel \text{gen}(E_1.true \text{ ':'}) \parallel E_2.code$ $E_1.true := E.false;$ $E_1.false := E.true;$ $E.code := E_1.code$
$E \rightarrow ( E_1 )$	$E_1.true := E.true;$
$E \rightarrow id_1 \text{ relop } id_2$	$E_1.false := E.false;$ $E.code := E_1.code$
$E \rightarrow \text{true}$	$E.code := \text{gen}(\text{'goto' } E.true)$
$E \rightarrow \text{false}$	$E.code := \text{gen}(\text{'goto' } E.false)$

## 3.8 CASE STATEMENTS

The “switch” or “case” statement is available in a variety of languages. The switch-statement syntax is as shown below :

### Switch-statement syntax

**switch** *expression*

**begin**

**case** *value:statement*

**case** *value:statement*

...

**case** *value:statement*

**default** : *statement*

**end**

There is a selector expression, which is to be evaluated, followed by  $n$  constant values that the expression might take, including a default “value” which always matches the expression if no other value does. The intended translation of a switch is code to:

1. Evaluate the expression.
2. Find which value in the list of cases is the same as the value of the expression.
3. Execute the statement associated with the value found.

Step (2) can be implemented in one of several ways :

- By a sequence of conditional **goto** statements, if the number of cases is small.
- By creating a table of pairs, with each pair consisting of a value and a label for the code of the corresponding statement. Compiler generates a loop to compare the value of the expression with each value in the table. If no match is found, the default (last) entry is sure to match.

- If the number of cases  $s$  is large, it is efficient to construct a hash table.
- There is a common special case in which an efficient implementation of the  $n$ -way branch exists. If the values all lie in some small range, say  $i_{\min}$  to  $i_{\max}$ , and the number of different values is a reasonable fraction of  $i_{\max} - i_{\min}$ , then we can construct an array of labels, with the label of the statement for value  $j$  in the entry of the table with offset  $j - i_{\min}$  and the label for the default in entries not filled otherwise. To perform switch,

evaluate the expression to obtain the value of  $j$ , check the value is within range and transfer to the table entry at offset  $j - i_{\min}$ .

## Syntax-Directed Translation of Case Statements:

Consider the following switch statement:

```

switch  $E$  begin
  case  $V_1:S_1$  case  $V_2:S_2$ 
    . . .
  case  $V_{n-1}:S_{n-1}$ 
  default :  $S_n$ 
end

```

This case statement is translated into intermediate code that has the following form :

### Translation of a case statement

```

                                code to evaluate  $E$  into  $t$ 
                                goto test
L1:                            code for  $S_1$ 
                                goto next
L2:                            code for  $S_2$ 
                                goto next
                                . . .
L $n-1$ :                        code for  $S_{n-1}$ 
                                goto next
L $n$ :                            code for  $S_n$ 
                                goto next
test :                          if  $t = V_1$  goto L1
                                if  $t = V_2$  goto L2
                                . . .
                                if  $t = V_{n-1}$  goto L $n-1$ 
next :                          goto L $n$ 

```



To translate into above form :

- When keyword **switch** is seen, two new labels **test** and **next**, and a new temporary **t** are generated.
- As expression  $E$  is parsed, the code to evaluate  $E$  into **t** is generated. After processing  $E$  , the jump **goto test** is generated.
- As each **case** keyword occurs, a new label  $L_i$  is created and entered into the symbol table. A pointer to this symbol-table entry and the value  $V_i$  of case constant are placed on a stack (used only to store cases).
- Each statement **case** $V_i$ :  $S_i$  is processed by emitting the newly created label  $L_i$ , followed by the code for  $S_i$  , followed by the jump **goto next**.
- Then when the keyword **end** terminating the body of the switch is found, the code can be generated for the n-way branch. Reading the pointer-value pairs on the case stack from the bottom to the top, we can generate a sequence of three-address statements of the form

```

case  $V_1$   $L_1$ 
case  $V_2$   $L_2$ 
...
case  $V_{n-1}$   $L_{n-1}$ 
case  $t$   $L_n$ 
label next

```

where  $t$  is the name holding the value of the selector expression  $E$ , and  $L_n$  is the label for the default statement.

## 3.9 BACKPATCHING

The easiest way to implement the syntax-directed definitions for boolean expressions is to use two passes. First, construct a syntax tree for the input, and then walk the tree in depth-first order, computing the translations. The main problem with generating code for boolean expressions and flow-of-control statements in a single pass is that during one single pass we may not know the labels that control must go to at the time the jump statements are generated. Hence, a series of branching statements with the targets of the jumps left unspecified is generated. Each statement will be put on a list of goto statements whose labels will be filled in when the proper label can be determined. We call this subsequent filling in of labels **backpatching**.

To manipulate lists of labels, we use three functions :

1. *makelist( $i$ )* creates a new list containing only  $i$ , an index into the array of quadruples; *makelist* returns a pointer to the list it has made.
2. *merge( $p_1, p_2$ )* concatenates the lists pointed to by  $p_1$  and  $p_2$ , and returns a pointer to the concatenated list.

3.  $backpatch(p, i)$  inserts  $i$  as the target label for each of the statements on the list pointed to by  $p$ .

## Boolean Expressions:

Let us construct a translation scheme suitable for producing quadruples for boolean expressions during bottom-up parsing. The grammar we use is the following:

1.  $E \rightarrow E_1 \text{or} M E_2$
2.     |  $E_1 \text{and} M E_2$
3.     |  $\text{not} E_1$
4.     |  $( E_1 )$
5.     |  $\text{id}_1 \text{relop id}_2$
6.     |  $\text{true}$
7.     |  $\text{false}$
8.  $M \rightarrow \epsilon$

Synthesized attributes  $truelist$  and  $falselist$  of nonterminal  $E$  are used to generate jumping code for boolean expressions. Incomplete jumps with unfilled labels are placed on lists pointed to by  $E.truelist$  and  $E.falselist$ .

Consider production  $E \rightarrow E_1 \text{and} M E_2$ . If  $E_1$  is false, then  $E$  is also false, so the statements on  $E_1.falselist$  become part of  $E.falselist$ . If  $E_1$  is true, then we must next test  $E_2$ , so the target for the statements  $E_1.truelist$  must be the beginning of the code generated for  $E_2$ . This target is obtained using marker nonterminal  $M$ .

Attribute  $M.quad$  records the number of the first statement of  $E_2.code$ . With the production  $M \rightarrow \epsilon$  we associate the semantic action

$$\{ M.quad := nextquad \}$$

The variable  $nextquad$  holds the index of the next quadruple to follow. This value will be backpatched onto the  $E_1.truelist$  when we have seen the remainder of the production  $E \rightarrow E_1 \text{and} M E_2$ . The translation scheme is as follows:

- |  |  |
|--|--|
| (1) $E \rightarrow E_1 \text{or} M E_2$  | $\{$ $backpatch ( E_1.falselist, M.quad);$<br>$E.truelist := merge( E_1.truelist, E_2.truelist);$<br>$E.falselist := E_2.falselist$ $\}$ |
| (2) $E \rightarrow E_1 \text{and} M E_2$ | $\{$ $backpatch ( E_1.truelist, M.quad);$<br>$E.truelist := E_2.truelist;$<br>$E.falselist := merge(E_1.falselist, E_2.falselist)$ $\}$  |
| (3) $E \rightarrow \text{not} E_1$       | $\{ E.truelist := E_1.falselist;$  |

	$E.falselist := E1.truelist; \}$
(4) $E \rightarrow (E1)$	$\{ E.truelist := E1.truelist; \\ E.falselist := E1.falselist; \}$
(5) $E \rightarrow id_1 rel op id_2$	$\{ E.truelist := makelist(nextquad); \\ E.falselist := makelist(nextquad + 1); \\ emit('if' id_1.place rel op id_2.place 'goto_') \\ emit('goto_') \}$
(6) $E \rightarrow true$	$\{ E.truelist := makelist(nextquad); \\ emit('goto_') \}$
(7) $E \rightarrow false$	$\{ E.falselist := makelist(nextquad); \\ emit('goto_') \}$
(8) $M \rightarrow \epsilon$	$\{ M.quad := nextquad \}$

## Flow-of-Control Statements:

A translation scheme is developed for statements generated by the following grammar :

1.  $S \rightarrow \text{if } E \text{ then } S$
2.      $| \text{if } E \text{ then } S \text{ else } S$
3.      $| \text{while } E \text{ do } S$
4.      $| \text{begin } L \text{ end}$
5.      $| A$
6.  $L \rightarrow L ; S$
7.      $| S$

Here  $S$  denotes a statement,  $L$  a statement list,  $A$  an assignment statement, and  $E$  a boolean expression. We make the tacit assumption that the code that follows a given statement in execution also follows it physically in the quadruple array. Else, an explicit jump must be provided.

## Scheme to implement the Translation:

The nonterminal  $E$  has two attributes  $E.truelist$  and  $E.falselist$ .  $L$  and  $S$  also need a list of unfilled quadruples that must eventually be completed by backpatching. These lists are pointed to by the attributes  $L.nextlist$  and  $S.nextlist$ .  $S.nextlist$  is a pointer to a list of all conditional and unconditional jumps to the quadruple following the statement  $S$  in execution order, and  $L.nextlist$  is defined similarly.

The semantic rules for the revised grammar are as follows:

$E \rightarrow \text{if } E \text{ then } M_1 S_1 N \text{ else } M_2 S_2$   
 $\{ \text{backpatch}(E.truelist, M_1.quad); \\ \text{backpatch}(E.falselist, M_2.quad);$

$$S.nextlist := merge (S1.nextlist, merge (N.nextlist, S2.nextlist)) \}$$

Then backpatch the jumps when  $E$  is true to the quadruple  $M1.quad$ , which is the beginning of the code for  $S1$ . Similarly, we backpatch jumps when  $E$  is false to go to the beginning of the code for  $S2$ . The list  $S.nextlist$  includes all jumps out of  $S1$  and  $S2$ , as well as the jump generated by  $N$ .

- |     |  |  |
|-----|--|--|
| (2) | $N \rightarrow \varepsilon$                          | $\{ N.nextlist := makelist( nextquad );$<br>$emit('goto\_') \}$  |
| (3) | $M \rightarrow \varepsilon$                          | $\{ M.quad := nextquad \}$   |
| (4) | $S \rightarrow \text{if } E \text{ then } MS1$       | $\{ backpatch( E.truelist, M.quad);$<br>$S.nextlist := merge( E.falselist, S1.nextlist) \}$  |
| (5) | $S \rightarrow \text{while } M1 E \text{ do } M2 S1$ | $\{ backpatch( S1.nextlist, M1.quad);$<br>$backpatch( E.truelist, M2.quad);$<br>$S.nextlist := E.falselist$<br>$emit( 'goto' M1.quad ) \}$ |
| (6) | $S \rightarrow \text{begin } L \text{ end}$          | $\{ S.nextlist := L.nextlist \}$   |
| (7) | $S \rightarrow A$                                    | $\{ S.nextlist := nil \}$  |

The assignment  $S.nextlist := \text{nil}$  initializes  $S.nextlist$  to an empty list.

- |     |                          |  |
|-----|--------------------------|--|
| (8) | $L \rightarrow L1 ; M S$ | $\{ backpatch( L1.nextlist, M.quad);$<br>$L.nextlist := S.nextlist \}$ |
|-----|--------------------------|--|

The statement following  $L1$  in order of execution is the beginning of  $S$ . Thus the  $L1.nextlist$  list is backpatched to the beginning of the code for  $S$ , which is given by  $M.quad$ .

- |     |                   |                                  |
|-----|-------------------|----------------------------------|
| (9) | $L \rightarrow S$ | $\{ L.nextlist := S.nextlist \}$ |
|-----|-------------------|----------------------------------|

### 3.10 PROCEDURE CALLS

The procedure is such an important and frequently used programming construct that it is imperative for a compiler to generate good code for procedure calls and returns. The run-time routines that handle procedure argument passing, calls and returns are part of the run-time support package.

Let us consider a grammar for a simple procedure call statement

- $S \rightarrow \text{call id}( Elist )$
- $Elist \rightarrow Elist , E$
- $Elist \rightarrow E$

#### Calling Sequences:

The translation for a call includes a calling sequence, a sequence of actions taken on entry to and exit from each procedure. The falling are the actions that take place in a calling sequence :

- When a procedure call occurs, space must be allocated for the activation record of the called procedure.
- The arguments of the called procedure must be evaluated and made available to the called procedure in a known place.
- Environment pointers must be established to enable the called procedure to access data in enclosing blocks.
- The state of the calling procedure must be saved so it can resume execution after the call.
- Also saved in a known place is the return address, the location to which the called routine must transfer after it is finished.
- Finally a jump to the beginning of the code for the called procedure must be generated. For example, consider the following syntax-directed translation

$$S \rightarrow \text{call id}(Elist)$$

$$\quad \{ \text{for each item } p \text{ on } queue \text{ do}$$

$$\quad \quad \text{emit}('param' p);$$

$$\quad \quad \text{emit}('call' id, place) \}$$

(2)  $Elist \rightarrow Elist, E$

$$\quad \{ \text{append } E.place \text{ to the end of } queue \}$$

(3)  $Elist \rightarrow E$

$$\quad \{ \text{initialize } queue \text{ to contain only } E.place \}$$

- Here, the code for S is the code for Elist, which evaluates the arguments, followed by a **param** statement for each argument, followed by a **call** statement.
- queue is emptied and then gets a single pointer to the symbol table location for the name that denotes the value of E.