

B. Tech Degree VI Semester (Supplementary) Examination September 2010

CS 604 MICROPROCESSOR SYSTEM DESIGN

(2002 Scheme)

Time: 3 Hours		Maximum Marks	
I.	(a) (b)	With a neat diagram explain the architecture of 80286. What are the different addressing modes of 8086? Give example for each.	(10) (10)
II.	(a) (b)	OR Draw a neat diagram and explain maximum mode configuration of 8086. What are the different types of interrupts in 8086 microprocessor? Explain the need of interrupt priority management.	(10) (10)
III.	(a) (b)	Explain the role of segment descriptor registers and tables in 80386 microprocessor. Explain the architecture of 80486 with the help of a diagram. OR	(10) (10)
IV.	(a) (b)	What is the need of virtual memory? How paging can be effectively used in protected mode operation of 80386 microprocessor? Compare and contrast the architecture of 80386 and 80486 microprocessors.	(12) (8)
v.	(a) (b)	Explain the implementation of branch prediction in Pentium processor. With the help of a block diagram explain the architecture of Pentium processor. Give the architectural difference between Pentium III and Pentium IV microprocessor. OR	(8) (12)
VI.	(a) (b) (c)	Explain floating point unit in Pentium. What is super scalar architecture? Explain. What are the different properties of RISC processors? What are the merits of RISC processors over CISC processors?	(6) (6) (8)
VII.	(a) (b)	List the functional categories of instruction with example. Write a program to check whether a number is prime or not. OR	(10) (10)
VIII.	(a) (b)	List the different branching instruction in 8086 instruction set with examples. Write a program to find the largest of n nos.	(8) (10)
IX.	(a) (b)	Draw the block diagram and explain the working of 16550 UART. Briefly explain the various components on the motherboard. OR	(8) (12)
X.	(a) (b)	Write short notes on BIOS and Boot Process.	(5) 3 = 15)