[4261]-112

## Seat

No.

## F.E. (Semester - II) Examination, 2012 <br> BASIC ELECTRONICS ENGINEERING (2008 Pattern)

Time : 2 Hours

> Instructions: 1) Neat diagrams must be drawn wherever necessary.
> 2) Black figures to the right indicate full marks.
> 3) Use of logarithmic tables slide rule, Mollier charts, electronic pocket calculator and steam tables is allowed.
> 4) Assume suitable data, if necessary.

1. A) Compare and contrast full-wave rectifier using two diodes and full-wave rectifier using four diodes.
B) The data sheet of JFET gives $\mathrm{I}_{\mathrm{DSS}}=10 \mathrm{~mA}$ and $\mathrm{V}_{\mathrm{GS} \text { (off) }}=-8 \mathrm{~V}$. Using these
values, determine the drain current for $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V},-1 \mathrm{~V}$ and -4 V .
C) Write short notes on Bar Graph and Matrix display.

OR
2. A) For Zener voltage regulator, if Iz min $=2 \mathrm{~mA}, \mathrm{I} \mathrm{z}_{\max }=20 \mathrm{~mA}, \mathrm{Vz}=4.7 \mathrm{~V}$. Determine the range of input voltage over which output voltage remains constant. Rs. $=1 \mathrm{k} \Omega, \mathrm{R}_{\mathrm{L}}=1 \mathrm{k} \Omega, \mathrm{Zz}=0 \Omega$.
B) Explain operation of BJT as a switch with neat circuit diagram and waveforms.
C) With neat construction diagram explain the working of TRIAC. Also draw its characteristics.
3. A) Draw and explain the operation of following gates using CMOS devices :

1) NAND gate
2) NOT gate
6
B) Draw the diagram of $1: 8$ demultiplexer. What is the relation between number of select lines and outputs?

4
C) An Op-amp is used in inverting mode with $R_{1}=1 \mathrm{~K} \Omega, \mathrm{R}_{\mathrm{F}}=10 \mathrm{~K} \Omega, \mathrm{~V}_{\mathrm{Cc}}=+/-15 \mathrm{~V}$. Calculate the output voltage for (1) 140 mV (2) 2.1 V .
4. A) What is full adder ? Explain the working of full adder with the help of truth table and give equation for sum and carry. ..... 6
B) What is an operational amplifier? Draw the neat block diagram and explain its working. ..... 6
C) Define oscillator. Find frequency of oscillations of Wien-bridge oscillator with $R=50 \mathrm{~K} \Omega$ and $\mathrm{C}=0.001 \mathrm{nF}$. ..... 4
5. A) Explain the working of alarm annunciator and PID controller. ..... 6
B) Write a short note on two wire transmitter. ..... 4
C) Draw the block diagram of basic communication system and explain each block in detail. ..... 6
OR
6. A) What is the need of modulation ? Compare AM and FM. ..... 6
B) Compare Co-axial cable media with fiber optic cable media. ..... 4
C) Draw the block diagram of electronic weighing machine and explain its operation. ..... 6

