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	me	LE PROPERTIE DE LA COMPANION D	
		emester B.Tech. Degree (Reg./Sup:/lmp. – Including Part Time Examination, November 2015 (2006 and Earlier Admn.) PTEC2K/EC2K 504: LINEAR INTEGRATED CIRCUITS	e)
Tin	ne : :	Hours Max. Marks	: 100
		Instruction: Answer all questions.	
		PART-A	
١	. a)	Explain the offset nulling technique used in opamp.	5
	b)	Define unity gain bandwidth of an opamp. Also explain the significance of unity gain B.W.	5
	c)	List out typical CMOS opamp parameters.	5
	d)	Explain the advantage of differential amplifier over single input amplifier.	5
*	e)	Explain the working of inverting amplifier with proper circuit diagram.	5
	f)	Explain the working of 4 quadrant multipliers.	5
	g)	Draw the circuit diagram of all pass filter and explain why we need all pass ilter.	5
	h)	Differentiate :  1) Active and passive filters  2) Analog and digital filters.	5
		PART-B	
11	۵۱	Evaluin and derive the equation for	

- II. a) Explain and derive
  - 1) Slew rate
  - 2) CMRR
  - 3) Input offset current.

OR

b) Define input bias current. Derive the equation of output offset voltage due to input bias current. Also explain how to nullify the effect of input bias current.

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111.	. a)	Explain the operation of cross coupled pair differential amplifier with neat circuit diagram. Also listout the advantages of cross coupled pair differential	537
		amplifier.	15
	b)	Explain the working of wide swing constant transconductance differential amplifier with neat circuit diagram.	15
IV.	a)	Define oscillator. Explain wein bridge oscillator with neat circuit diagram and derive the equation for $f_o$ (frequency of oscillation) and $R_F/R_1$ .	15
		OR CONTRACTOR OF THE PROPERTY	
	b)	<ul> <li>i) Explain voltage to current converter using proper circuit diagram and equation.</li> </ul>	7
		<ul> <li>Explain log amplifier with neat circuit diagram and derive the equation for output voltage.</li> </ul>	8
V.	a)	Derive the equation for transfer function of twin-T notch filter with neat circuit diagram. Also obtain the equation for Q factor.	15
		OR	
	b)	Draw the circuit diagram of a second order Butterworth Sallenkey configuration H.P.F. and derive the equation for transfer function H(S)?	15