

Code No. : 5145/S

B.E. 3/4 (ECE) I Semester (Suppl.) Examination, June 2012 MICROPROCESSORS AND INTERFACING

Time: 3 Hours] [Max. Marks: 75

Note: Answer all questions from Part A.

Answer any five questions from Part B.

PART-A (25 Marks)

- 1. Explain the function of pin READY on 8086 processor.
- 2. Explain the functions of control flags of 8086.
- 3. Write instruction format of 8086 with all fields.
- 4. Explain the following alignment directions of 8086.
- t) EVEN

- 2) ORG
- 5. Compare minimum mode and maximum mode operation of 8086.
- 6. Write an assembly language program to interface DAC to 8086.
- 7. Draw the Block diagram of 8251.
- 8. What is the difference between 8085 and 8086? Explain DMA interfacing concept.
- 9. List and describe in general terms the steps an 8086 will take when it responds to an interrupt.
- 10. Why are the port lines of programmable port devices automatically put in input mode when the device is first powered up or reset?

PART-B (50 Marks)

11. Explain the following instructions with suitable examples:

10

a) XLAT

b) LES

c) LEA

d) LDS

e) LAHF

12.	a)	sound by the macro PUSH-ALL in the sound by the soun		
		o da ka	SI	5
	b)	Explain about command and mode i	nstruction of 8251.	5
13.	Ex	plain operational modes of 8255.		10
14.	Dr	aw the block diagram of keyboard an	d display controller and explain in detail.	10
15.		raw the 7-segment display interface not the ALP.	nechanism diagram with 8086 and explain	10
16.	Dr.		explain briefly the function of 4 major	
17.	Wı		80 9 (5	
	a)	Differentiate between a UART and U	SART.	
	b)	Describe how the 8087 and 8088 w	ork together to load a long -real data item	
		two BCD digits in the byte.	BCD byte to the ASCII characters for the	
			menter sen kritike menter kanen. Eli så kanna å så sær un ken ill skriber i litter i hinn å kriber i skriber i Hennesperansperanserende	